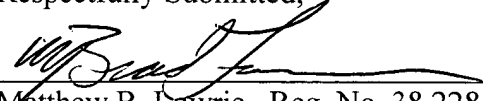


- A2  
Concl.
28. The pipelined control unit of claim 29, wherein the first set of registers includes three 8-bit registers.
29. The pipelined control unit of claim 29, wherein the second set of registers includes three 8-bit registers.
30. The pipelined control unit of claim 29, wherein the pipeline unit is a double pipeline unit.
31. The pipelined control unit of claim 32, wherein the double pipeline unit includes two channels, each channel being 16 bits wide.
32. The pipelined control unit of claim 29, wherein the pipeline unit is a quad pipeline unit.
33. The pipelined control unit of claim 29, further comprising a microcontroller coupled to the control circuit.
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**REMARKS**

Each of the new claims is fully supported by the specification and no new matter has been added.

Respectfully Submitted,

  
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